Before the first claim, No. 41, change "CLAIMS:" to "We claim:".

Amend Claim 52 so that the claims are as follows:

1 - 40.   (Canceled)

41.   (Previously amended) A decoder system comprising:

a control unit capable of performing multiple tasks and capable of being interrupted during at least one of the tasks to perform at least one other of the tasks;

a first data buffer comprising a video input buffer and an audio input buffer;

a stream demultiplexer for receiving an incoming data stream comprising data packets each comprising at least one of (i) encoded video data and a video header that contains video timing information for the encoded video data and (ii) encoded audio data and an audio header that contains audio timing information for the encoded audio data, the stream demultiplexer operating

(a) to demultiplex and depacketize the data packets without interrupting the control unit,

(b) to send the encoded video data to the video input buffer for storage there without the video timing information,

(c) to provide, for use by the control unit, video messages which identify where the encoded video data is stored in the video input buffer and which also deal with the video timing information, and

(d) to send the encoded audio data to the audio input buffer for storage there;

a video decoder that decodes the encoded video data to produce decoded video data utilizing video instructions provided from the control unit as to where the encoded video data is stored in the video input buffer;

an audio decoder that decodes the encoded audio data to produce decoded audio data;

a second data buffer comprising (a) a video output buffer that stores the decoded video data and (b) an audio output buffer that stores the decoded audio data; and

a memory management unit responsive to signals from the control unit for controlling transfer of the decoded audio data to and from the audio output buffer.

42.   (Previously amended) A decoder system as in Claim 41 wherein the control unit is interrupted in response to a synchronization signal for reading the video messages provided by the system demultiplexer and for providing the video instructions to the video decoder.

43.   (Previously amended) A decoder system as in Claim 42 wherein the control unit reads each video message provided by the system demultiplexer during the period between consecutive occurrences of the synchronization signal and provides a responsive one of the video instructions to the video decoder during that period.

44.   (Previously presented) A decoder system as in Claim 42 wherein the video decoder decodes the encoded video data in response to the synchronization signal.

45.   (Previously presented) A decoder system as in Claim 44 wherein the video decoder decodes the encoded video data in response to an occurrence of the synchronization signal only when an unserviced one of the video instructions from the control unit is present at the video decoder.

46.   (Previously presented) A decoder system as in Claim 44 wherein the control unit provides each video instruction to the video decoder in response to an occurrence of the synchronization signal for use by the video decoder in response to the next occurrence of the synchronization signal.

47.   (Previously presented) A decoder system as in Claim 42 wherein the synchronization signal is provided at a substantially fixed frequency.

48.   (Previously amended) A decoder system as in Claim 41 wherein the video messages comprise tags which contain the timing information and the addresses of storage locations for the encoded video data in the video input buffer.

49.   (Previously presented) A decoder system as in Claim 48 wherein the video instructions comprise task definition packets which contain the addresses of storage locations for the encoded video data in the video input buffer.

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50.   (Previously presented) A decoder system as in Claim 41 wherein the video timing information comprises at least one of a presentation time stamp and a decoder time stamp.

51.   (Previously amended) A decoder system as in Claim 41 wherein the encoded audio data is stored in the audio input buffer without the audio timing information, the stream demultiplexer also operating to provide, for use by the control unit, audio messages which deal with the audio timing information.

52.   (Currently amended) A decoder system as in Claim 51 41 wherein the audio messages also indicate where the encoded audio data is stored in the audio input buffer.

53.   (Previously presented) A decoder system as in Claim 51 wherein the audio decoder detects audio sync words in the encoded audio data.

54.   (Previously presented) A decoder system as in Claim 53 wherein the control unit utilizes the audio timing information and the audio sync words provided from the audio decoder to detect presentation times for the decoded audio data.

55.   (Previously presented) A decoder system as in Claim 51 wherein the timing informations comprise time stamps.

56.   (Previously presented) A decoder system as in Claim 41 further including:
        a timer for maintaining local current time; and
        a clock generator coupled to the timer for providing clock references for the decoder system.

57.   (Previously amended) A decoder system as in Claim 41 wherein the first data buffer includes a message queue for storing the video messages provided by the stream demultiplexer for use by the control unit.

58.   (Previously presented) A decoder system as in Claim 41 further including:
        a video output processor for processing the decoded video data to produce processed video data suitable for video presentation; and

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an audio output processor for processing the decoded audio data to produce processed audio data suitable for digital-to-analog conversion.

59.   (Previously amended) A decoder system as in Claim 58 wherein, in response to a synchronization signal generated by the video output processor, the control unit is interrupted for reading the video messages provided by the stream demultiplexer and for providing the video instructions to the video decoder.

60.   (Previously presented) A decoder system as in Claim 59 wherein the video decoder decodes the encoded video data in response to the synchronization signal.

61.   (Canceled)

62.   (Previously presented) A decoder system as in Claim 41 for use in an audio/video system that includes:
        a video display for displaying the processed video data in picture form; and
        an audio digital-to-analog converter for converting the processed audio data into analog audio data.

63.   (Previously amended) A decoder system as in Claim 41 wherein the data packets are constituted in data bytes.

64.   (Previously presented) A decoder system as in Claim 63 wherein the data bytes comprise Digital Versatile Disc bytes or Digital Video Broadcast bytes.

65.   (Previously presented) A decoder system as in Claim 63 further including a Digital Versatile Disc-Digital Signal Processor interface that converts a Digital Versatile Disc bit stream into a byte stream supplied to the stream demultiplexer as its incoming data stream.

66.   (Previously presented) A decoder system as in Claim 63 further including a Network Port that converts a Digital Video Broadcast bit stream into a byte stream supplied to the stream demultiplexer as its incoming data stream.

67.   (Previously amended) A method comprising:

receiving an incoming data stream comprising data packets each comprising at least one of (i) encoded video data and a video header that contains video timing information for the encoded video data and (ii) encoded audio data and an audio header that contains audio timing information for the encoded audio data;

demultiplexing and depacketizing the data packs without interrupting a control unit capable of performing multiple tasks and capable of being interrupted during at least one of the tasks to perform at least one other of the tasks;

storing the encoded video data in a video input buffer without the video timing information;

providing, for use by the control unit, video messages which identify where the encoded video data is stored in the video input buffer and which also deal with the video timing information;

decoding the encoded video data to produce decoded video data using video instructions provided from the control unit as to where the encoded video data is stored in the video input buffer;

storing the encoded audio data in an audio input buffer;

decoding the encoded audio data to produce decoded audio data; and

utilizing a memory management unit responsive to signals from the control unit to control transfer of the decoded audio data to and from an audio output buffer.

68.   (Previously presented) A method as in Claim 67 further including:

generating a synchronization signal; and

interrupting the control unit in response to the synchronization signal for causing the control unit to read the video messages and to generate the video instructions.

69.   (Previously presented) A method as in Claim 68 wherein the control unit reads each video message during the period between consecutive occurrences of the synchronization signal and generates a responsive one of the video instructions during that period.

70.   (Previously presented) A method as in Claim 68 wherein the decoding of the encoded video data occurs in response to the synchronization signal.

71.   (Previously presented) A method as in Claim 70 wherein the decoding of the encoded video data occurs in response to an occurrence of the synchronization signal only when an unserviced one of the video instructions is in existence.

72.   (Previously presented) A method as in Claim 70 wherein the control unit generates each of the video instructions in response to an occurrence of the synchronization signal for use in decoding the encoded video data at the next occurrence of the synchronization signal.

73.   (Previously presented) A method as in Claim 68 wherein the synchronization signal is provided at substantially a fixed frequency.

74.   (Previously amended) A method as in Claim 67 wherein the encoded audio data is stored in the audio input buffer without the audio timing information, the method further including providing, for use by the control unit, audio messages dealing with the timing information.

75.   (Previously presented) A method as in Claim 74 wherein the audio messages also indicate where the encoded audio data is stored in the audio input buffer.

76.   (Previously amended) A method as in Claim 67 further including detecting sync words in the encoded audio data.

77.   (Previously presented) A method as in Claim 76 further including utilizing the audio timing information and the sync words to determine presentation times for the decoded audio data.

78.   (Previously presented) A method as in Claim 67 further including:

processing the decoded video data to produce processed video data suitable for video presentation; and

processing the decoded audio data to produce processed audio data suitable for digital-to-analog conversion.

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79.   (Previously presented) A method as in Claim 78 further including:

utilizing a video output processor that processes the decoded video data to generate a synchronization signal; and

interrupting the control unit in response to the synchronization signal for reading the video messages and for generating the video instructions.

80.   (Previously presented) A method as in Claim 79 wherein the decoding of the encoded video data occurs in response to the synchronization signal.

81.   (Previously presented) A method as in Claim 78 further including:

displaying the processed video data in picture form; and

converting the processed audio data into analog audio data.

82.   (Previously presented) A method as in Claim 67 further including converting an incoming bit stream into a byte stream that constitutes the incoming data stream.

83.   (Canceled)

84.   (Previously amended) A decoder system as in Claim 89 wherein the first data buffer is external to an integrated circuit containing the control unit.

85.   (Canceled)

86.   (Previously amended) A method as in Claim 92 further including accessing the video and audio input buffers external to an integrated circuit containing the control unit.

87 and 88.   (Canceled)

89.   (Previously amended) A decoder system as in Claim 41 wherein the memory management unit also controls (a) transfer of the encoded video data to and from the video input buffer in response to signals from the control unit and (b) transfer of the encoded audio data to and from the audio input buffer in response to signals from the control unit.

90 and 91.   (Canceled)

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92.   (Previously amended) A method as in Claim 67 further including utilizing the memory management unit (a) to control transfer of the encoded video data to and from the video input buffer in response to signals from the control unit and (b) to control transfer of the encoded audio data to and from the audio input buffer in response to signals from the control unit.

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